

AFRL SBIR/STTR Debriefing Worksheet (FINAL)
Small Business Innovation Research Program

Proposal Number: F161-088-1073

Topic Number: AF161-088

Company: Zeidman Technologies

Proposal Title: Integrated Code Base and High Performance Embedded Computing Tool

Remarks

This proposal is considered outside the scope of the topic and thus not relevant to the topic. The topic is seeking a software tool or tool suite that can convert high level software languages (like C++ and Matlab) into a hardware description language, such as VHDL, to specify the hardware architecture (expression of circuit logic forming a computer architecture). The technology proposed is for creating a Real-time Operation System which is software used to manage computer hardware and software resources, in real-time, based on input from a user. In other words, the topic is for a tool that helps describe what the computer hardware is, while the proposed technology is for software that controls how the computer hardware operates. These are two completely different types of technology. As such, this proposal is not relevant to the chosen topic and thus was identified as such.

